

# VIGGEN TWO System on Module

powered by NXP i.MX6UL ARM® Cortex®-A7 with Crypto Engine

ARM® Cortex®-A7 @ 528 Mhz, 128 KB L2 cache

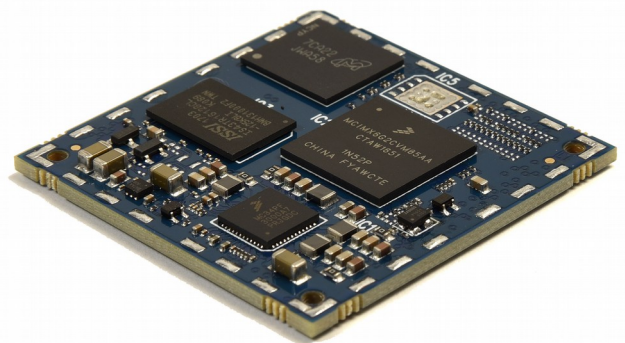
Small size 43x43mm 250 pin LGA

Security Features: TRNG, Crypto Engine (AES/TDES/SHA/RSA with DPA), Tamper Monitor, Secure Boot, SIMV2/EVMSIM X 2, OTF DRAM Encryption, PCI4.0 pre-certification

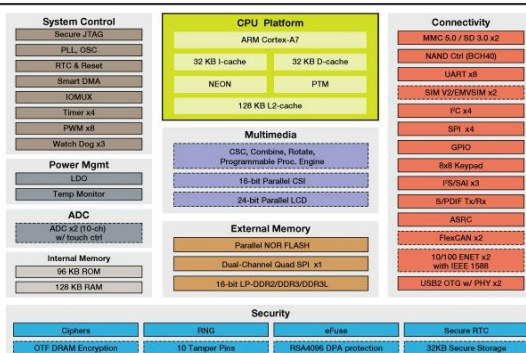
Shield with tamper detection

24-bit Parallel LCD Display up to WXGA (1366x768)

8-bit Parallel Camera Sensor Interface



i.MX 6UltraLite Applications Processor Block Diagram



Optional

2x MMC 4.5/SD 3.0/SDIO Port

2x USB 2.0 OTG, HS/FS, Device or Host with PHY

2x 10/100 Ethernet with IEEE 1588

2x 12-bit ADC, up to 10 input channel total, with resistive touch controller (4-wire/5-wire)

10x GPIOs

5x UARTS

2x CAN Interface

2x I2C

JTAG interface

Ideal Engine for General Purpose Control Units, Security Systems, Gateways for the IoT networks

Security Features: Crypto Engine, Anti-Tamper, Secure Boot...

Easy soldering component, 43x43mm LGA package, with Shield

Supports Security systems, Appliances and Gateways

Development Platform with Sub GHz Radio, 2.4GHz and uSD